

INFORMATION DISCLOSURE CITATION
IN AN APPLICATION

(Use several sheets if necessary)

Docket Number (Continued)

MEGIC - 99-005

Application Number

09/523,990

Applicant

Mon-Shiung Lin et al.

Filing Date

03/13/00

Group Art Unit

2876

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	ALNO DATE IF APPROPRIATE
DW	5970321	10/19/99	Hively	438	123	9/25/97
	5973395	10/26/99	Suzuki et al.	257	692	4/29/97
	5461545	10/24/95	Leroy et al.	361	765	9/13/94
	5641997	6/24/97	Ohta et al.	257	788	9/13/94
	4300184	11/10/81	Colla	361	397	7/11/79
	5479049	12/26/95	Aoki et al.	257	642	9/15/93
	5834340	11/10/98	Sawai et al.	438	126	11/24/97
	5866949	2/2/99	Schueller	257	778	10/8/97
	5925934	7/20/99	Lim	257	778	1/4/96
	5951804	9/14/99	Kweon et al.	156	244.12	6/30/97
	5955784	9/21/99	Chiu	257	737	2/22/94

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
					YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Portion of Pages, Etc.)

TW	John Baliga, "Ball Grid Arrays: The High-Pincount Workhorse", Semiconductor Int'l, Sept. 1999, pp. 1-2.
RV	John Baliga, "Defining the CSP", Chip Scale Technology, Sept. 1999, pp. 6-8.

EXAMINER

D. O. Lee

DATE CONSIDERED

2.6.00

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.